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APPLICANT(S) Joseph J. Nahas et al. GROUP ART UNIT: 2818
 APPLN. NO.: 10/656,646 EXAMINER: Son Luu Mai
 FILED: September 5, 2003 CONFIRMATION NO. 8098
 TITLE: CIRCUIT FOR WRITE FIELD DISTURBANCE CANCELLATION IN
 AN MRAM AND METHOD OF OPERATION

Certificate of Transmission under 37 C.F.R 1.8

I hereby certify that this correspondence is being
 facsimile transmitted to the Patent and Trademark
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on 9-20-04

Pat Thomas
 Signature

Pat Thomas

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ISSUE OF PATENT TO ASSIGNEE PURSUANT TO 37 CFR 3.81

Mail Stop ISSUE FEE
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Sir:

Payment of the Issue Fee of this application is concurrently made with the
 request that the application issue in the name of Freescale Semiconductor, Inc. An
 assignment of all right, title and interest in the instant application from Motorola, Inc.
 to Freescale Semiconductor, Inc. has been previously filed for recordation in
 accordance with 37 CFR 3.11, but it is not believed to have been recorded by the
 USPTO as of yet.

SEND CORRESPONDENCE TO:

Freescale Semiconductor, Inc.
 Customer Number: 23125

Respectfully submitted,

By: Robert L. King
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